

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,076,749 B2
APPLICATION NO. : 10/709807
DATED : July 11, 2006
INVENTOR(S) : Kemerer et al.

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1

Line 5, delete "BACKGROUND OF INVENTION"

Line 7, delete "1. Field of the Invention" and insert (centered and printed in capital letters) -- FIELD OF THE INVENTION --

Line 12, delete "2. Background of the Invention" and insert (centered and printed in capital letters) -- BACKGROUND OF THE INVENTION --

Column 2

Line 28, delete "etc," and insert -- etc. --

Line 46, delete "Calif." and insert -- Ca--

Line 47, delete "CaA" and insert --Ca --

Column 3

Line 6, delete "or"

Line 39, delete "(FET) transistor" and insert -- (FET) --

Column 4

In Table I, in the R11 entry, at the end of that line, insert --] --

In Table I, in the R13 through R18 entries, delete the "{" and insert -- / --

Columns 5 and 6

In Table II, delete the four arrows pointing down

Column 6

Line 33, delete "DRC" and insert -- DCR --

Line 34, delete "DCRCormat" and insert -- DCR format --

Line 35, delete "DCRsCofware" and insert -- DCR software --

Line 39, begin a new paragraph after the period.

Column 7

Line 41, delete "In step, 95" and insert -- In step 195, --

Column 8

Line 67, delete "Productivity" and insert -- Normalized Score --

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Column 9

Line 36, delete "Score= $e^{-[(EV-RV)/WF]}$ " and insert-- Score = $e^{-[(EV-RV)/WF]}$ --

Line 62, delete "a connecting" and insert --connecting --

Signed and Sealed this

Thirty-first Day of July, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS
Director of the United States Patent and Trademark Office